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PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = ASANO

First Name = MASANARI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07982623</u>	<u>5361220</u>	150	11/27/1992	DISCRETE COSINE TRANSFORMATION WITH REDUCED COMPONENTS	ASANO, MASANARI
<u>08932750</u>	<u>6014467</u>	150	09/17/1997	HIGH SPEED, HIGH PRECISION IMAGE COMPRESSION	ASANO, MASANARI
<u>09537389</u>	<u>6351291</u>	150	03/29/2000	Image Processing Apparatus For An On-Screen-Display Which Displays One Image Over Another Image-	ASANO, MASANARI
<u>09813035</u>	Not Issued	71	03/21/2001	Image processor with the closed caption function and image processing method	ASANO, MASANARI
<u>09899157</u>	Not Issued	71	07/06/2001	Method of and an apparatus for processing images	ASANO, MASANARI

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[Apple II comp.sys.apple2.programmer FAQs, Part 1/1](#)

... to supply a super-res desktop, Windows-like environment ... the 4 least significant bits

and did some **additional adjustment**. ... X and Y contain valid **data** before entry ...
[comp.sys.apple2.programmer](#) - Jun 21 2005, 7:17 am by rubyw...@swbell.net - 1 message - 1 author

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[comp.unix.sco Technical FAQ \(5/5\)](#)

... use of hardware handshaking requires **additional wiring**, but ... discussion of packetization

and sliding **window protocols** is ... modems, but may need **adjustment** in some ...
[comp.unix.sco.announce](#) - Sep 6 2000, 5:10 am by Steve Dunn - 1 message - 1 author

Probe 2.0: User comments

... well under MS Windows 3.x as a DOS **window**, although some **adjustment** of .PIF ... summaries

of the last several active channels, and **additional** menu options ...

[rec.radio.scanner](#) - Apr 1 1996, 11:02 pm by Larry Ledlow - 2 messages - 2 authors

DIGTRX 1.37 by PY4ZBZ Original Help (RV3BZ)

... A new map file is also created for **additional** repairs later. ... To allow a better **adjustment** of the ... DIGTRX now doesn't show more the DOS **windows** opened by the ...

[fido7.ru.sstv-digital](#) - Feb 17 2004, 11:40 pm by Alex Ronzhin - 1 message - 1 author

Designing a graphic e-meter

... For example, as the TA **adjustment** is automatic, there ... fast reads, and there is an

additional overshoot at ... reduce the power consumption (using Windows or Linux ...)

[alt.clearing.technology](#) - May 15 2000, 1:30 pm by Ralph Hilton - 1 message - 1 author

IBM Gives "FOSS" Free Access to 500 Patents

... and method that progressively prefetches **additional** lines to ... graphics display device

[US5245700 Adjustment of Z ... registry functions in a windows operating system](#) ...

[comp.os.linux.advocacy](#) - Jan 12 2005, 5:43 pm by Robert M. Stockmann - 44 messages - 17 authors

Project/Team Leader, Ph.D., OOA/OOD/OOP, DSP, avionics ...

... **Additional** areas of expertise include DSP for ... source code compilation, changes and

adjustment), administration and ... adaptive DSP Platforms: MS Windows, IBM SVM ...

misc.jobs.resumes - Mar 29 1998, 10:50 am by d...@writeme.com - 2 messages - 1 author

Zenith PA/PZ/PM & Tocom 5507MU Turn-on (Dealers Only)

... be noise sensitive, critical in timing **adjustment and causes ... falls out of the acceptance**

window, the entire ... This **additional garbling effect on the TV audio is ...**

rec.video.cable-tv - Jul 17 1996, 8:16 pm by Keith Knipschild - LI ,N.Y - 3 messages - 3 authors

LCD FAQ latest edition

... extra segments look just like **additional 5x7 dot ... correction circuitry to provide automatic contrast **adjustment**. ... be typed on when display **window** scrolling is ...**

alt.comp.hardware.homebuilt - Mar 12 1995, 7:34 am by Christopher J Burian - 4 messages - 3 authors

New CPUs on the horizon

... USB and Advanced Edition users, **additional options can ... roster of Editions, ViaVoice**

for **Windows, Release 10 ... of VoiceCenter allows **adjustment** of size, shape and ...**

stardock.discussion.technology - Nov 6 2002, 11:40 pm by David H. McCoy - 164 messages - 20 authors

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1 [Level set and PDE methods for computer graphics](#)

David Breen, Ron Fedkiw, Ken Museth, Stanley Osher, Guillermo Sapiro, Ross Whitaker
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes**
SIGGRAPH '04

Publisher: ACM PressFull text available: [pdf\(17.07 MB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#)

Level set methods, an important class of partial differential equation (PDE) methods, define dynamic surfaces implicitly as the level set (iso-surface) of a sampled, evolving nD function. The course begins with preparatory material that introduces the concept of using partial differential equations to solve problems in computer graphics, geometric modeling and computer vision. This will include the structure and behavior of several different types of differential equations, e.g. the level set eq ...

2 [The elements of nature: interactive and realistic techniques](#)

Oliver Deussen, David S. Ebert, Ron Fedkiw, F. Kenton Musgrave, Przemyslaw Prusinkiewicz, Doug Roble, Jos Stam, Jerry Tessendorf
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes**
SIGGRAPH '04

Publisher: ACM PressFull text available: [pdf\(17.65 MB\)](#) Additional Information: [full citation](#), [abstract](#)

This updated course on simulating natural phenomena will cover the latest research and production techniques for simulating most of the elements of nature. The presenters will provide movie production, interactive simulation, and research perspectives on the difficult task of photorealistic modeling, rendering, and animation of natural phenomena. The course offers a nice balance of the latest interactive graphics hardware-based simulation techniques and the latest physics-based simulation techni ...

3 [GPGPU: general purpose computation on graphics hardware](#)

David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Buck, Cliff Woolley, Aaron Lefohn
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes**
SIGGRAPH '04

Publisher: ACM PressFull text available: [pdf\(63.03 MB\)](#) Additional Information: [full citation](#), [abstract](#)

The graphics processor (GPU) on today's commodity video cards has evolved into an

extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex and pixel processing units that support vector operations up to full IEEE floating point precision. High level languages have emerged for graphics hardware, making this computational power accessible. Architecturally, GPUs are highly parallel s ...

4 High dynamic range imaging

◆ Paul Debevec, Erik Reinhard, Greg Ward, Sumanta Pattanaik
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: [pdf\(20.22 MB\)](#) Additional Information: [full citation](#), [abstract](#)

Current display devices can display only a limited range of contrast and colors, which is one of the main reasons that most image acquisition, processing, and display techniques use no more than eight bits per color channel. This course outlines recent advances in high-dynamic-range imaging, from capture to display, that remove this restriction, thereby enabling images to represent the color gamut and dynamic range of the original scene rather than the limited subspace imposed by current monitor ...

5 Special issue: AI in engineering

◆ D. Sriram, R. Joobbani
April 1985 **ACM SIGART Bulletin**, Issue 92

Publisher: ACM Press

Full text available: [pdf\(8.79 MB\)](#) Additional Information: [full citation](#), [abstract](#)

The papers in this special issue were compiled from responses to the announcement in the July 1984 issue of the SIGART newsletter and notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers received from over six countries. About half the papers were received over the computer network.

6 Real-time volume graphics

◆ Klaus Engel, Markus Hadwiger, Joe M. Kniss, Aaron E. Lefohn, Christof Rezk Salama, Daniel Weiskopf
August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes SIGGRAPH '04**

Publisher: ACM Press

Full text available: [pdf\(7.63 MB\)](#) Additional Information: [full citation](#), [abstract](#)

The tremendous evolution of programmable graphics hardware has made high-quality real-time volume graphics a reality. In addition to the traditional application of rendering volume data in scientific visualization, the interest in applying these techniques for real-time rendering of atmospheric phenomena and participating media such as fire, smoke, and clouds is growing rapidly. This course covers both applications in scientific visualization, e.g., medical volume data, and real-time rendering, ...

7 DOD standard transmission control protocol

◆ Jon Postel
October 1980 **ACM SIGCOMM Computer Communication Review**, Volume 10 Issue 4

Publisher: ACM Press

Full text available: [pdf\(4.83 MB\)](#) Additional Information: [full citation](#), [references](#)

8 Seeing, hearing, and touching: putting it all together

Brian Fisher, Sidney Fels, Karon MacLean, Tamara Munzner, Ronald Rensink

 August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes SIGGRAPH '04**

Publisher: ACM Press

Full text available:  pdf(20.64 MB) Additional Information: [full citation](#)

9 Link and channel measurement: A simple mechanism for capturing and replaying 

 **wireless channels**

Glenn Judd, Peter Steenkiste

August 2005 **Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to wireless network design and analysis E-WIND '05**

Publisher: ACM Press

Full text available:  pdf(6.06 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Physical layer wireless network emulation has the potential to be a powerful experimental tool. An important challenge in physical emulation, and traditional simulation, is to accurately model the wireless channel. In this paper we examine the possibility of using on-card signal strength measurements to capture wireless channel traces. A key advantage of this approach is the simplicity and ubiquity with which these measurements can be obtained since virtually all wireless devices provide the req ...

Keywords: channel capture, emulation, wireless

10 System-level power optimization: techniques and tools 

 Luca Benini, Giovanni de Micheli

April 2000 **ACM Transactions on Design Automation of Electronic Systems (TODAES),**

Volume 5 Issue 2

Publisher: ACM Press

Full text available:  pdf(385.22 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic systems consisting of a hardware platform and software layers. We consider the three major constituents of hardware that consume energy, namely computation, communication, and storage units, and we review methods of reducing their energy consumption. We also study models for analyzing the energy cost of software, and methods for energy-efficient software design and compilation. This survey ...

11 The Personal Presence System—hardware architecture 

 M. Lukacs

October 1994 **Proceedings of the second ACM international conference on Multimedia**

Publisher: ACM Press

Full text available:  pdf(957.84 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The Personal Presence System (PPS) experimental prototype is being designed to support multiparty multimedia visual services which use advanced video combining techniques. This paper is a companion to another paper in this proceedings: "The Personal Presence System—A Wide Area Network Service Resource for the Real Time Composition of Multipoint Multimedia Communications" which contains a detailed service description. This paper describes the architecture of the A ...

12 Trace-driven memory simulation: a survey 

 Richard A. Uhlig, Trevor N. Mudge

June 1997 **ACM Computing Surveys (CSUR)**, Volume 29 Issue 2

Publisher: ACM Press

Full text available:  pdf(636.11 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

As the gap between processor and memory speeds continues to widen, methods for evaluating memory system designs before they are implemented in hardware are becoming increasingly important. One such method, trace-driven memory simulation, has been the subject of intense interest among researchers and has, as a result, enjoyed rapid development and substantial improvements during the past decade. This article surveys and analyzes these developments by establishing criteria for evaluating trac ...

Keywords: TLBs, caches, memory management, memory simulation, trace-driven simulation

13 Using 2-domain partitioned OBDD data structure in an enhanced symbolic simulator 

 Tao Feng, Li-C Wang, Kwang-Ting (Tim) Cheng, Chih-Chang (Andy) Lin

October 2005 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 10 Issue 4

Publisher: ACM Press

Full text available:  pdf(419.60 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In this article, we propose a symbolic simulation method where Boolean functions can be efficiently manipulated through a 2-domain partitioned OBDD data structure. The functional partition is applied by automatically exploring the key decision points implicitly built inside a circuit. The partition can help to significantly reduce the OBDD sizes, solving problems that could not be solved with monolithic OBDD data structure. We demonstrate the performance of the approach through the symbolic simu ...

Keywords: Formal verification, equivalence checking, symbolic simulation

14 Virtual machine monitors: Xen and the art of virtualization 

 Paul Barham, Boris Dragovic, Keir Fraser, Steven Hand, Tim Harris, Alex Ho, Rolf Neugebauer, Ian Pratt, Andrew Warfield

October 2003 **Proceedings of the nineteenth ACM symposium on Operating systems principles**

Publisher: ACM Press

Full text available:  pdf(168.76 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Numerous systems have been designed which use virtualization to subdivide the ample resources of a modern computer. Some require specialized hardware, or cannot support commodity operating systems. Some target 100% binary compatibility at the expense of performance. Others sacrifice security or functionality for speed. Few offer resource isolation or performance guarantees; most provide only best-effort provisioning, risking denial of service. This paper presents Xen, an x86 virtual machine monit ...

Keywords: hypervisors, paravirtualization, virtual machine monitors

15 Making operating systems more robust: Improving the reliability of commodity operating systems 

 Michael M. Swift, Brian N. Bershad, Henry M. Levy

October 2003 **Proceedings of the nineteenth ACM symposium on Operating systems principles**

Publisher: ACM Press

Full text available: [pdf\(262.78 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Despite decades of research in extensible operating system technology, extensions such as device drivers remain a significant cause of system failures. In Windows XP, for example, drivers account for 85% of recently reported failures. This paper describes Nooks, a *reliability subsystem* that seeks to greatly enhance OS reliability by isolating the OS from driver failures. The Nooks approach is practical: rather than guaranteeing complete fault tolerance through a new (and incompatible) OS ...

Keywords: I/O, device drivers, protection, recovery, virtual memory

16 Special issue: Game-playing programs: theory and practice

 M. A. Bramer
April 1982 **ACM SIGART Bulletin**, Issue 80

Publisher: ACM Press

Full text available: [pdf\(9.23 MB\)](#) Additional Information: [full citation](#), [abstract](#)

This collection of articles has been brought together to provide SIGART members with an overview of Artificial Intelligence approaches to constructing game-playing programs. Papers on both theory and practice are included.

17 Space-time scheduling of instruction-level parallelism on a raw machine

 Walter Lee, Rajeev Barua, Matthew Frank, Devabhaktuni Srikrishna, Jonathan Babb, Vivek Sarkar, Saman Amarasinghe

October 1998 **ACM SIGPLAN Notices , ACM SIGOPS Operating Systems Review , Proceedings of the eighth international conference on Architectural support for programming languages and operating systems ASPLOS-VIII**, Volume 33 , 32 Issue 11 , 5

Publisher: ACM Press

Full text available: [pdf\(1.79 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Increasing demand for both greater parallelism and faster clocks dictate that future generation architectures will need to decentralize their resources and eliminate primitives that require single cycle global communication. A Raw microprocessor distributes all of its resources, including instruction streams, register files, memory ports, and ALUs, over a pipelined two-dimensional mesh interconnect, and exposes them fully to the compiler. Because communication in Raw machines is distributed, com ...

18 File system usage in Windows NT 4.0

 Werner Vogels
December 1999 **ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating systems principles SOSP '99**, Volume 33 Issue 5

Publisher: ACM Press

Full text available: [pdf\(1.75 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We have performed a study of the usage of the Windows NT File System through long-term kernel tracing. Our goal was to provide a new data point with respect to the 1985 and 1991 trace-based File System studies, to investigate the usage details of the Windows NT file system architecture, and to study the overall statistical behavior of the usage data. In this paper we report on these issues through a detailed comparison with the older traces, through details on the operational characteristics and ...

 Visualizing geospatial data

Theresa Marie Rhyne, Alan MacEachren, Theresa-Marie Rhyne

August 2004 **Proceedings of the conference on SIGGRAPH 2004 course notes**

SIGGRAPH '04

Publisher: ACM Press

Full text available:  pdf(14.01 MB) Additional Information: [full citation](#), [abstract](#)

This course reviews concepts and highlights new directions in GeoVisualization. We review four levels of integrating geospatial data and geographic information systems (GIS) with scientific and information visualization (VIS) methods. These include:• Rudimentary: minimal data sharing between the GIS and Vis systems• Operational: consistency of geospatial data• Functional: transparent communication between the GIS and Vis systems• Merged: one comprehensive toolkit environmentW ...

20 Power reduction techniques for microprocessor systems

 Vasanth Venkatachalam, Michael Franz

September 2005 **ACM Computing Surveys (CSUR)**, Volume 37 Issue 3

Publisher: ACM Press

Full text available:  pdf(602.33 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Power consumption is a major factor that limits the performance of computers. We survey the "state of the art" in techniques that reduce the total power consumed by a microprocessor system over time. These techniques are applied at various levels ranging from circuits to architectures, architectures to system software, and system software to applications. They also include holistic approaches that will become more important over the next decade. We conclude that power management is a ...

Keywords: Energy dissipation, power reduction

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